



PATENT
5500-90300/TT5390

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/755,742

Filed: January 12, 2004

Inventors:

Gregory William Smaus

Raghuram S. Tupuri

Gerald D. Zuraski

Title: Method and Processor
Including Logic for Storing
Traces within a Trace Cache

§ Examiner: Kim, Daniel Y
§ Group/Art Unit: 2185
§ Atty. Dkt. No: 5500-90300
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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Robert C. Kowert

Printed Name



Signature

June 16, 2006

Date

RESPONSE TO OFFICE ACTION OF MARCH 21, 2006

Mail Stop Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of March 21, 2006, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.